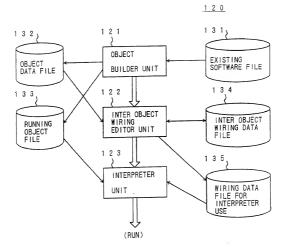


Fig.2



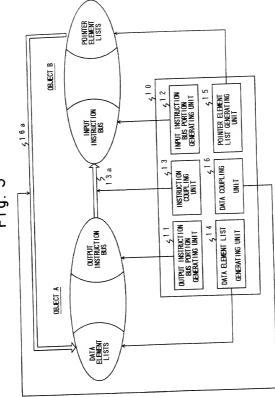


Fig. 3

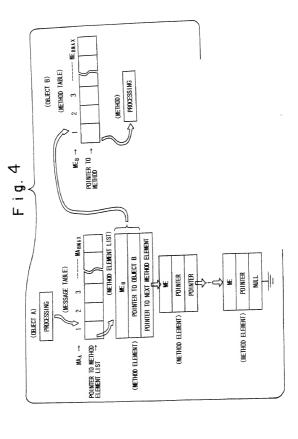


Fig. 5

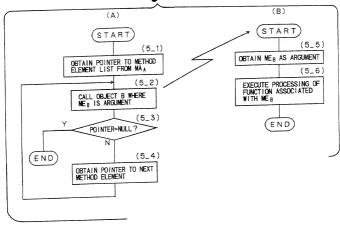
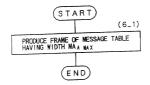


Fig. 6



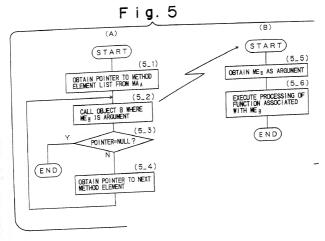
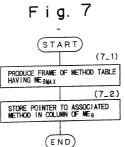
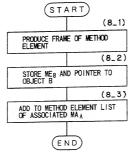


Fig. 6









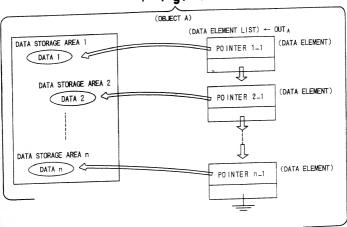
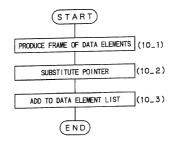


Fig. 10

COYSTEND . DESIGN



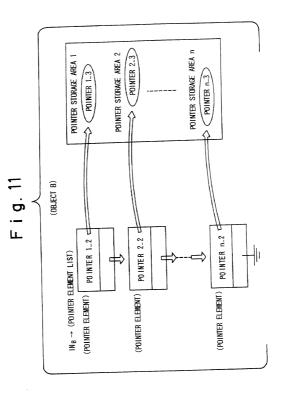


Fig. 12

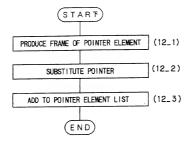


Fig. 13

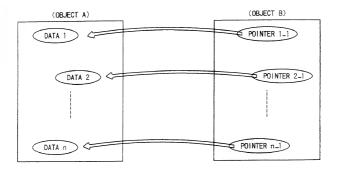


Fig. 14

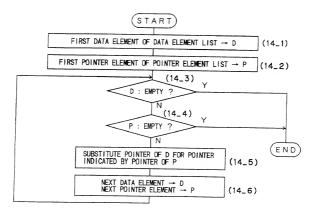


Fig. 15

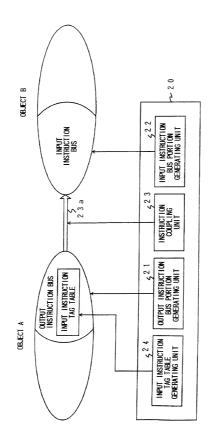


Fig. 16

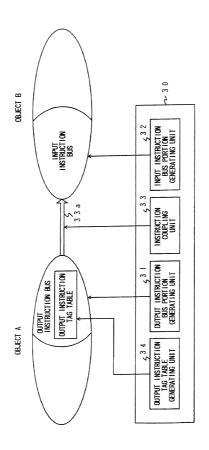


Fig. 17

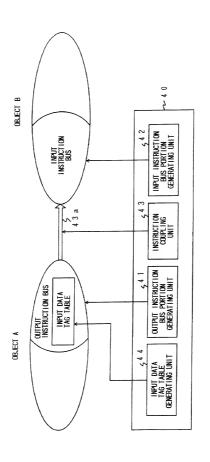


Fig. 18

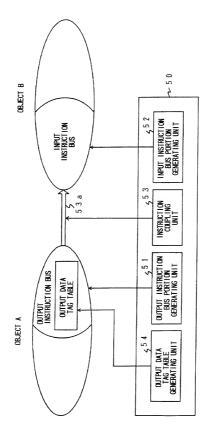


Fig. 19

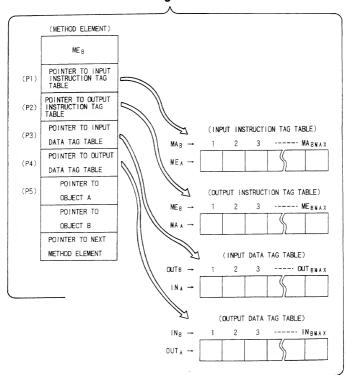


Fig. 20

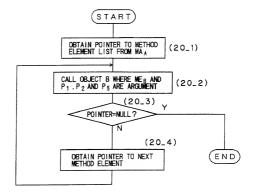
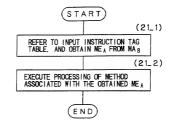


Fig. 21



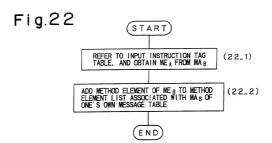


Fig.23

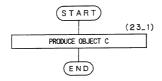


Fig. 24

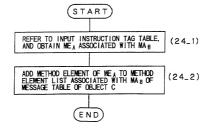


Fig. 25

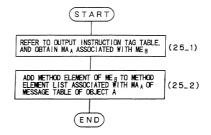
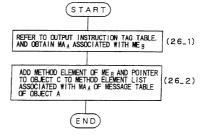


Fig. 26



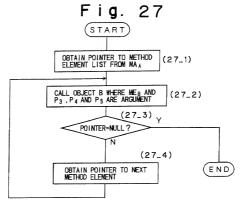


Fig. 28

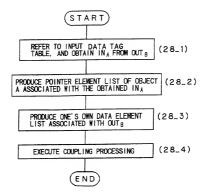


Fig. 29

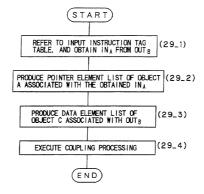
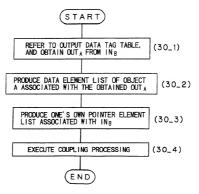


Fig. 30



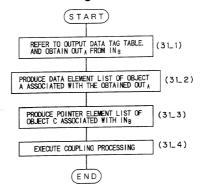
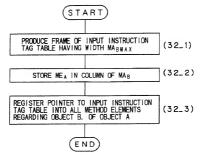


Fig. 32



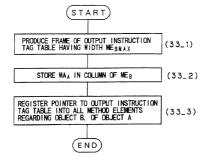
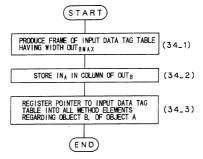


Fig. 34



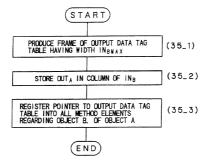


Fig. 36

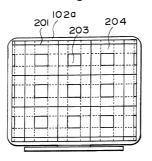


Fig. 37

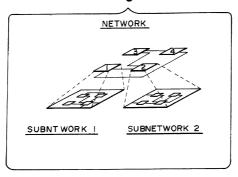


Fig.38(A)

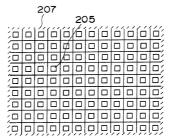


Fig. 38(B)

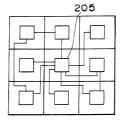


Fig.39(A)

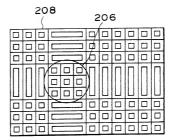


Fig.39(B)

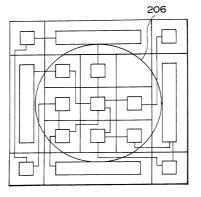


Fig. 40(A)

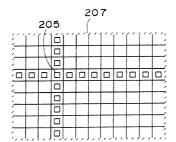
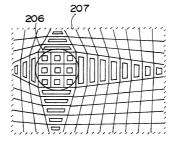


Fig.40(B)



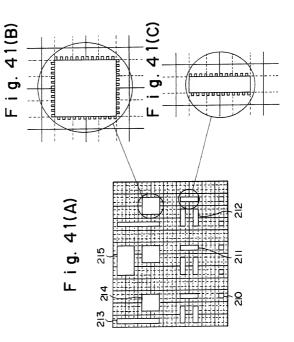


Fig. 42

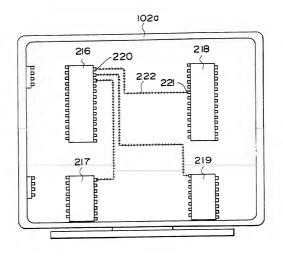


Fig. 43(A)

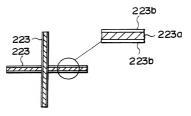
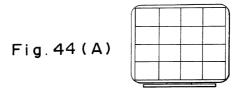
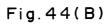


Fig. 43(B)







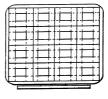


Fig. 44(C)

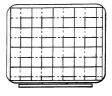
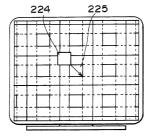
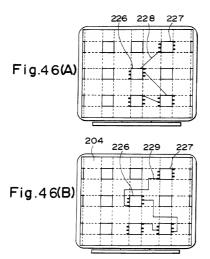


Fig. 45





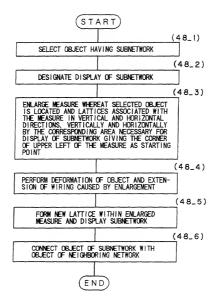
F i g. 47 (A)

STARTING POINT OF ENLARGEMENT													
OBJECT HAVING SUBNETWORK													
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F i g. 47 (B)

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HEIGHT INCREASING											
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			П								

Fig. 48



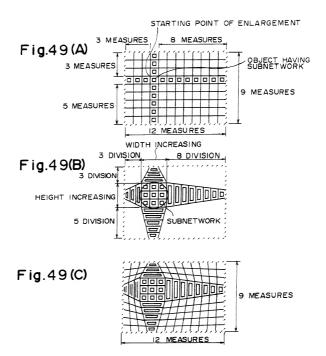
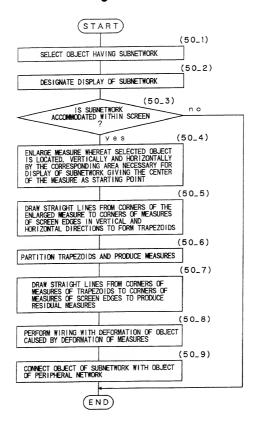
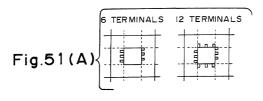
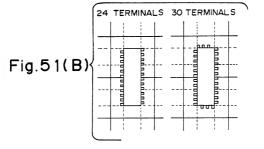


Fig.50

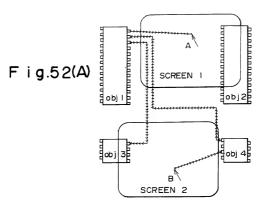






48 TERMINALS

Fig.51(C)



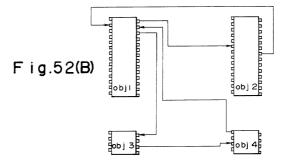
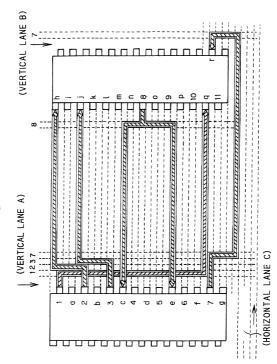
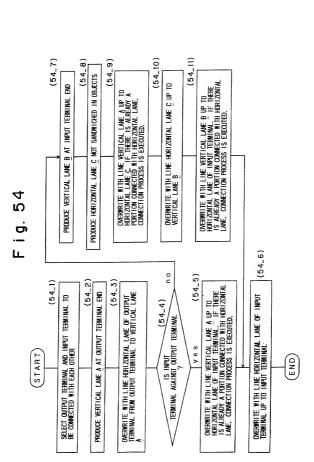


Fig.53





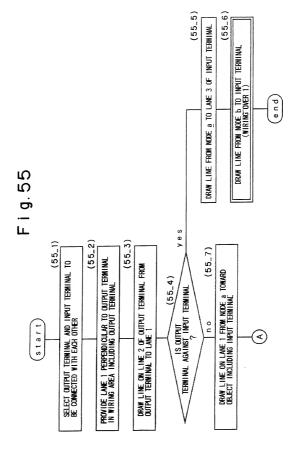
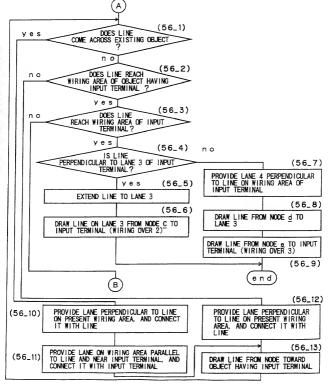


Fig.56



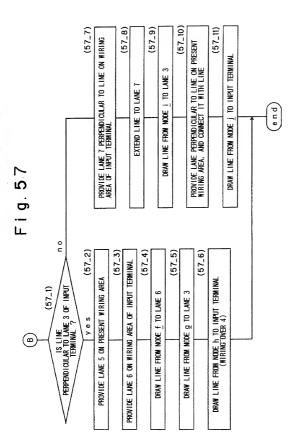


Fig. 58

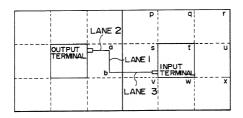


Fig. 59

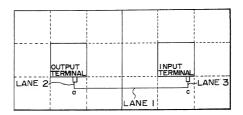


Fig. 60

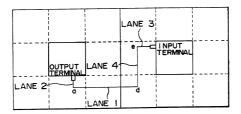


Fig. 61

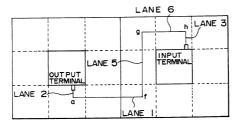
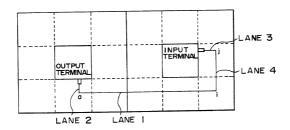


Fig. 62



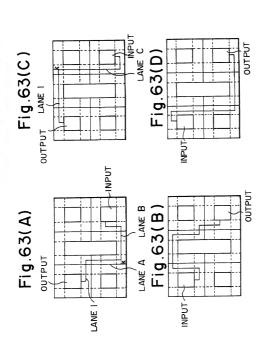


Fig. 64

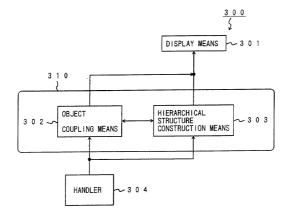


Fig. 65

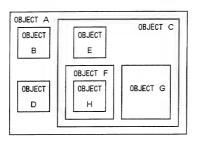


Fig.66

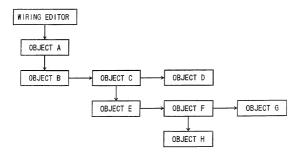


Fig. 67

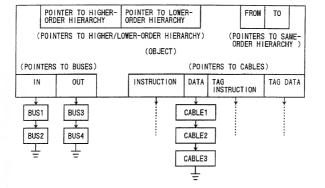


Fig.68

(BUS)

POINTER TO SUBSTANTIAL OBJECT
POINTER TO BUS OF SUBSTANTIAL OBJECT
POINTER TO NEXT BUS
OTHER DATA

Fig. 69

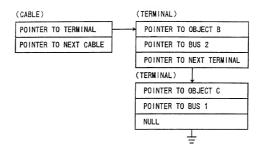


Fig. 70

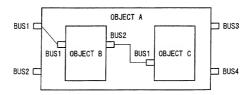


Fig. 71

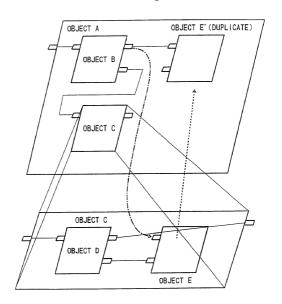


Fig. 72

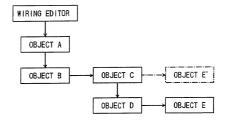


Fig. 73

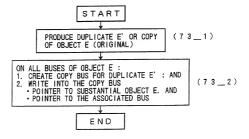


Fig. 74

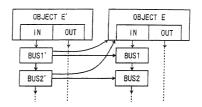
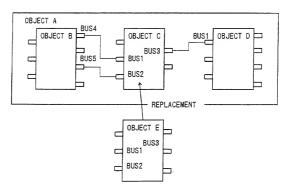
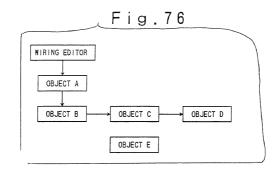
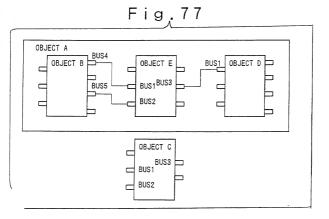


Fig. 75







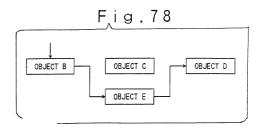


Fig. 79

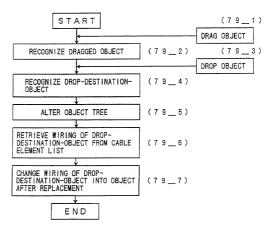


Fig. 80

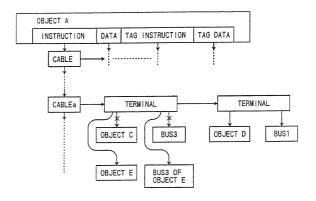


Fig. 81

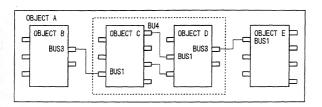


Fig.82

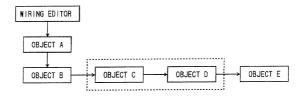


Fig.83

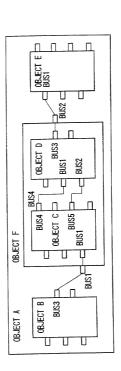


Fig. 84

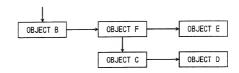


Fig. 85

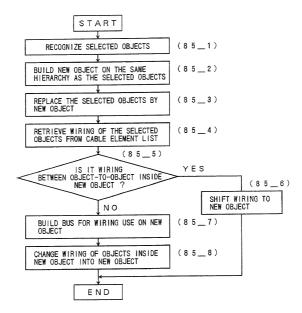


Fig. 86

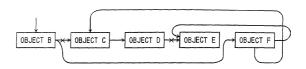


Fig. 87

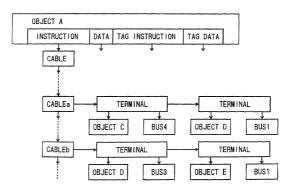


Fig. 88

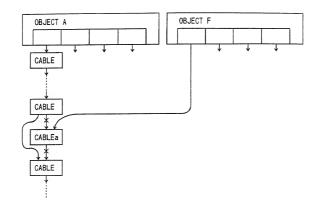


Fig. 89

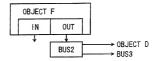


Fig.90

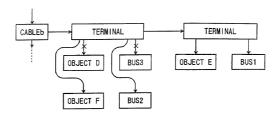


Fig. 91

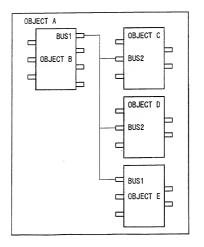


Fig. 92

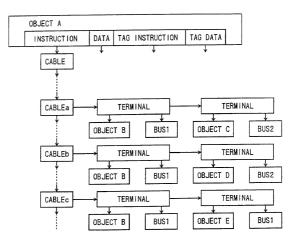


Fig. 93

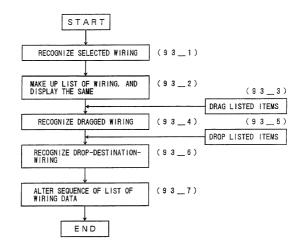


Fig. 94

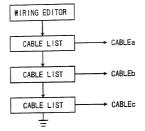


Fig. 95

OBJECT C : BUS2
OBJECT D : BUS2
OBJECT E : BUS1

Fig.96

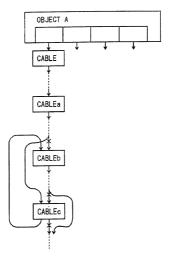


Fig. 97

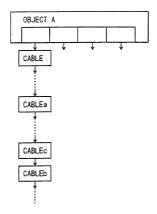


Fig. 98

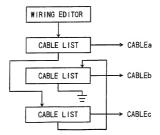


Fig.99

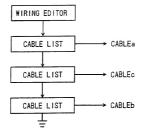


Fig. 100

OBJECT B : BUS1	OBJECT C : BUS2
OBJECT B : BUS1	OBJECT E : BUS1
OBJECT B : BUS1	OBJECT D : BUS2

Fig. 101

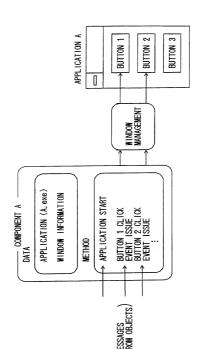


Fig. 102

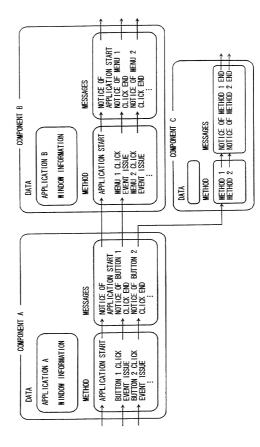


Fig. 103

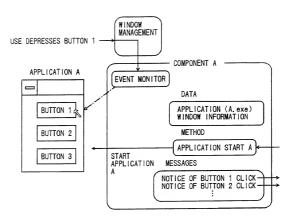


Fig. 104

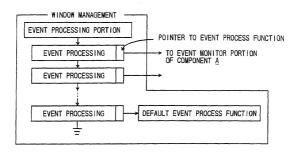


Fig. 105

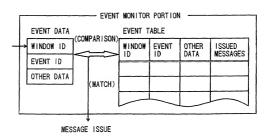


Fig. 106

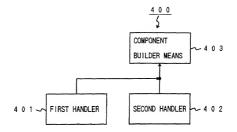


Fig. 107

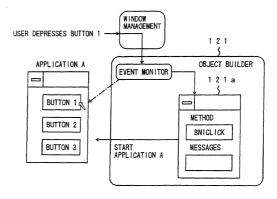


Fig. 108

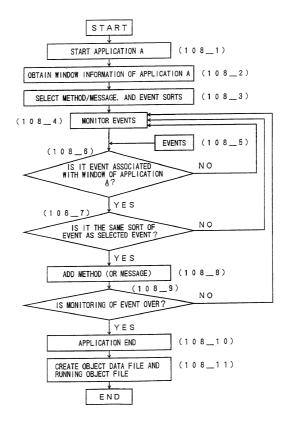


Fig.109

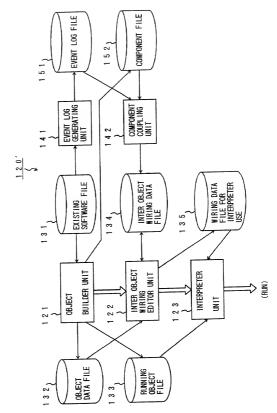


Fig. 110

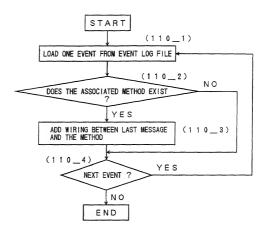


Fig. 111

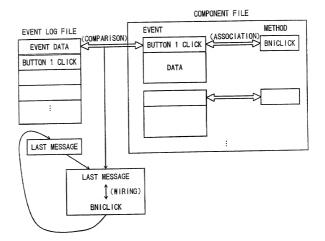


Fig. 112

(A) HEADER

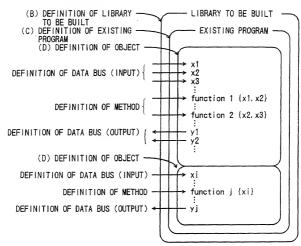


Fig. 113

	ITEMS	KEYWORDS	REMARKS
	TIEMS	NETHUNUS	KEMAKKS
(A)	PROJECT PROJECT NAME PATH OF COMPILER SYSTEM PATH OF FIRSTSIGHT SYSTEM PATH OF USER AREA	LSIBuilderProjec LSIBuilderProjec MSVCRoot CoreRoot UserRoot	
(B)	DEFINITION OF ARCHIVES NAME OF ARCHIVES PATH OF LIB PATH OF DLL	Archives ArchivesName LibPath DIIPath	
(c)	NAME OF LIBRARY TO BE BUILT COMPILE MODE DEFINITION OF #define AND typedef	LibName Debug Header	·-·-·-
(D)	DEFINITION OF LSI NAME OF LSI COLOR OF LSI DATA BUS NAME OF DATA CORRECTION PROCESS NAME OF DATA BUS TYPE OF VARIABLES DATA CORRECTION PROCESS DIRECT DEVELOPMENT INTO DefineConnector DISTINCTION BETWEEN INPUT AND OUTPUT COLOR OF BUS	Process Inline	TREE COLORS OF RGB (0-255) CODE OF FUNCTION input OR output
	INSTRUCTION INSTRUCTION BUS FUNCTION NAME OF ENTRY POINT MEANING OF RETURN VALUE INSTRUCTION PROCESS Cmd ? DIRECT DEVELOPMENT INTO Cmd OR Command COLOR OF BUS GLOBAL VARIABLES (CLOBAL VARIABLES INSIDE LSI) DEFINITION #define AND typedef INITIALIZATION PROCESS CONSTRUCTOR	Colo Variables Header	zero OR nonzero OR NUMERAL CODE OF FUNCTION yes/no CODE OF FUNCTION CODE OF FUNCTION
'[DESTRUCTOR	Destructor	CODE OF FUNCTION

Fig.114

										:			
					:					INPUT & OUTPUT			
		÷								JT &			
		:			/ALUE					IN			
		define			Return value					TYPE OF VARIABLE			
		OLOR		ABLE:						0F V			
ÆLP	NOI	LSI NAME LSI COLOR		NSTRUCTION BUS TABLE:	ENTRY				BLE:	TYPE			
MAKE	LSI INFORMATION	AME.		CT ION	BUS NAME	:	:		DATA BUS TABLE:	BUS NAME	:	:	
	<u>×</u>	IS		STRU					ATA B	BUS	•	•	
DISPLAY	2			<u>'</u> =									 \dashv
		₩.	E :	tion tion	Codata CLSI NAME	tion Stign	AME.	LSI NAME	at lon	-Cdata OLSI NAME	ction		
EDITING	NAME	ation ves N	AME I	Torma struc	YAME ,	struc	ra ves À	NAMET NAMET	itoriii istruc	NAME	ITOFM ISTRUC	ra	
	ect	10. 10. 10.		=. <u>=</u> .	25.	5.5.4 700	Darichives NAME		=.=- 700		- -	-Odata	
FILE	Oproject NAME	- δ (, 6 ,	7					